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## DESCRIPTION

## OPTICAL RECEIVER

## 5 TECHNICAL FIELD

The present invention relates to an optical receiver, and, more particularly to an optical receiver technology applied to very-high-speed/very-long-distance optical transmission systems and the like.

10

## BACKGROUND ART

In the very-high-speed/very-long-distance optical transmission systems by an Intensity Modulation/Direct Detection (IM/DD) method, an error correction code has been 15 mainly used as means for improvement of reception sensitivity of the optical receiver. However, in the recent rapid increase in a transmission volume and a transmission distance, application of a technology for further improvement of reception sensitivity of the optical 20 receiver is indispensable. There is a demand for a simple technology for improving reception sensitivity other than the error correction code.

Conventionally, as the technology for improving reception sensitivity of this type, there is an example 25 disclosed in a patent document 1 described below.

In the patent document 1, improvement of reception sensitivity is attained by providing k sets ( $n \times k$ ) discriminators that have different thresholds from one another with n discriminators having an identical threshold 30 as one set, an optical distributor that distributes a reception signal to the respective discriminators, and a selector that selects one of discrimination results of the respective discriminators and outputs the discrimination

result, performing majority judgment using the selector, and selecting a discrimination result with higher accuracy from the discrimination results of the respective discriminators.

5        In a nonpatent literature 1 described below, for example, as an application of the reception technology described in the patent document 1, improvement of sensitivity of a receiver is realized by subjecting one optical signal to wavelength division at a reception stage,  
10 providing discriminators for the respective optical signals divided, and adopting more probable data from discrimination results in the respective discriminators.

Patent Document 1

Japanese Patent Application Laid-Open No. 2003-234699

15        Nonpatent Literature 1  
Masahito Tomizawa, Yoshiaki Kisaka, Akira Hirano, and Yutaka Miyamoto, "Error correction without additional redundancy by novel optical receiver with diverse detection," in OSA Trends in Optics and Photonics (TOPS)  
20 vol. 70, Optical Fiber Communication Conference, Technical Digest, Post-conference Edition (Optical Society of America, Washington DC, 2002), WX7, pp. 368-370.

In the realization of the very-high-speed/very-long-distance optical transmission systems, improvement of sensitivity of the optical receiver is an important element and a technology that is simple and has an easy configuration is indispensable.

However, the invention described in the nonpatent document 1 uses a method of dividing a spectrum of an optical signal using an optical filter. Thus, the invention has a problem in that a division penalty in the optical filter tends to occur and sufficient improvement of sensitivity cannot be expected.

Since the invention also uses the optical filter in implementation, there is also a problem in that limitation on a configuration occurs in terms of dependency on a signal modulation system, stability with respect to 5 temperature, and the like.

Moreover, even if improvement of sensitivity of the optical receiver is realized by a minimum number of discriminators, it is necessary to control a discrimination threshold by preparing two sets of two discriminators (four 10 discriminators in total). Thus, the method is not a simple and easy method from the viewpoint of control of the discriminators or implementation.

In view of such circumstances, it is an object of the present invention to provide an optical receiver that 15 prevents occurrence of a division penalty due to an optical divider, eliminates dependency on a modulation system, and secures easiness of implementation.

#### DISCLOSURE OF INVENTION

An optical receiver according to one aspect of the present invention includes an optical divider that divides an optical input signal into a plurality of paths; an optical-to-electrical converter that converts the divided optical input signal into an electrical signal; a 25 discriminator that outputs a discrimination result obtained by discriminating the electrical signal output from the optical-to-electrical converter based on a predetermined threshold; and an operational circuit that performs a predetermined operation based on the discrimination result 30 output from the discriminator.

According to the present invention, the optical divider divides an optical input signal into a plurality of paths, the OE converters convert the optical input signals

after division into electrical signals, the discriminators output discrimination results obtained by discriminating, based on predetermined thresholds, the electrical signals output from the OE converters, and the operational circuit 5 performs a predetermined operation based on the discrimination results output from the discriminators.

#### BRIEF DESCRIPTION OF DRAWINGS

Fig. 1 is a block diagram of a structure of an optical receiver according to a first embodiment of the present 10 invention;

Fig. 2A is a diagram for explaining a flow of a discrimination result of the optical receiver shown in Fig. 1;

15 Fig. 2B is a diagram of a truth table of an OR circuit;

Fig. 2C is a diagram of a truth table of an AND circuit;

Fig. 3 is a graph of a bit error rate characteristic 20 with respect to a change in a threshold;

Fig. 4 is a block diagram of a structure of an optical receiver according to a second embodiment of the present invention;

25 Fig. 5 is a block diagram of a structure of an optical receiver according to a third embodiment of the present invention;

Fig. 6 is a block diagram of a structure of an optical receiver according to a fourth embodiment of the present invention; and

30 Fig. 7 is a block diagram of a structure of an optical receiver according to a fifth embodiment of the present invention.

BEST MODE(S) FOR CARRYING OUT THE INVENTION

Exemplary embodiments of an optical receiver according to the present invention are explained in detail below with reference to the accompanying drawings. The present  
5 invention is not limited by the embodiments.

First Embodiment

Fig. 1 is a block diagram of a structure of an optical receiver according to a first embodiment of the present invention. The optical receiver shown in the figure  
10 includes an optical amplifier 10 that amplifies an optical input signal, an optical divider 11 that divides the optical input signal into two path, that is, a path 1 and a path 2, optical-to electrical (OE) converters 12a and 12b that convert the optical input signals after division into  
15 electrical signals, discriminators 13a and 13b that output discrimination results (digital data signals) obtained by discriminating, based on predetermined thresholds  $V_{th1}$  and  $V_{th2}$ , the analog electrical signals output from the OE  
converters 12a and 12b, and an operational circuit 14 that  
20 performs a logical operation based on digital signals on the respective paths. The operational circuit 14 can be constituted by, for example, an OR circuit or an AND  
circuit.

Operations of the optical receiver shown in Fig. 1 are  
25 explained. In the figure, an optical signal transmitted from a not-shown optical transmitter or the like is input to this optical receiver. The optical input signal received is amplified by the optical amplifier 10 and, then, divided into the two paths, that is, the path 1 and the  
30 path 2, by the optical divider 11. The optical input signals divided into two paths are converted into electrical signals by the OE converters 12a and 12b and output to the discriminators 13a and 13b. The

discriminators 13a and 13b output discrimination results (digital data signals) obtained by discriminating the analog electrical signals based on the predetermined thresholds  $V_{th1}$  and  $V_{th2}$  to the operational circuit 14. The 5 operational circuit 14 outputs a processing result obtained by performing predetermined processing.

An output (a discrimination result) of the optical receiver at the time when the optical signals divided into the two paths have intersymbol interferences of the same 10 degree and Gaussian noises at output levels of the same degree, respectively, is examined below.

In this examination, it is assumed that characteristics, fluctuation in discrimination sensitivity, and the like of the discriminators 13a and 13b can be 15 neglected. It is assumed that, as to probability that a discrimination result concerning an arbitrary analog electrical signal is wrong, the Gaussian noises included in the optical signals on the respective paths are uncorrelated to each other such that mutual independency 20 between the discriminators 13a and 13b is maintained.

First, an output of the operational circuit 14 constituted by an OR circuit is examined.

Under these conditions, an error occurs in an output result of the operational circuit 14 in the following cases.

25 (a) In the case where logical "0" is input to an optical signal

When one of discrimination results of the discriminators 13a and 13b is wrong

30 When both discrimination results of the discriminators 13a and 13b are wrong

(b) In the case where logical "1" is input to an optical signal

When both discrimination results of the

discriminators 13a and 13b are wrong

An error rate  $BER_{OR}$  of an output result at the time when the operational circuit 14 is an OR circuit is considered using Figs. 2A and 2B. Fig. 2A is a diagram for 5 explaining a flow of a discrimination result of the optical receiver shown in Fig. 1. Fig. 2B is a diagram of a truth table of the OR circuit.

In Fig. 2A,  $P_1(1/0)$  shown at an output terminal of the discriminator 13a is probability that a discrimination 10 result is misjudged as logical "1" when logical "0" is input to the discriminator 13a.  $P_1(0/1)$  is probability that a discrimination result is misjudged as logical "0" when logical "1" is input to the discriminator 13a.

Similarly,  $P_2(1/0)$  shown at an output terminal of the 15 discriminator 13b is probability that a discrimination result is misjudged as logical "1" when logical "0" is input to the discriminator 13b.  $P_2(0/1)$  is probability that a discrimination result is misjudged as logical "0" when logical "1" is input to the discriminator 13b.

20 In Fig. 2A,  $P_{out}(1/0)$  shown at an output terminal of the operational circuit 14 is probability that a discrimination result among discrimination results processed by the operational circuit 14 based on the discrimination results of the discriminators 13a and 13b is 25 misjudged as logical "1" when logical "0" is input to an input terminal of the optical receiver. Similarly,  $P_{out}(0/1)$  is probability that the operational circuit 14 misjudges a discrimination result as logical "1" when logical "0" is input to the input terminal of the optical 30 receiver.

Therefore, an error rate  $BER_{OR}$  at the time when the operational circuit 14 is an OR circuit can be represented by

$$\begin{aligned}
BER_{OR} &= \frac{1}{2} P_{OUT}(1/0) + \frac{1}{2} P_{OUT}(0/1) \\
&= \frac{1}{2} \{P_1(1/0)P_2(1/0) + P_1(1/0)[1 - P_2(1/0)] \\
&\quad + [1 - P_1(1/0)]P_2(1/0)\} + \frac{1}{2} P_1(0/1)P_2(0/1)
\end{aligned} \tag{1}$$

In Equation (1), it is assumed that a mark rate, which is an occupied time ratio of the input signals "0" and "1" in a predetermined transmission time, is 50% (1/2).

5 Moreover, for easiness of examination, it is assumed that respective thresholds of the discriminators 13a and 13b are equal. In this case, it is possible to set  $V_{th1}$ ,  $V_{th2}$ , and  $V_{th}$  as  $V_{th1}=V_{th2}=V_{th}$ . The following relations are established among  $P_1(0/1)$ ,  $P_1(1/0)$ ,  $P_2(0/1)$ , and  $P_2(1/0)$ .

10  $P_1(1/0)=P_2(1/0)=P(1/0)$

$P_1(0/1)=P_2(0/1)=P(0/1)$

Thus, it is possible to simplify Equation (1) as

$$BER_{OR} = \frac{1}{2} [2P(1/0) - P(1/0)^2 + P(0/1)^2] \tag{2}$$

15 As it is seen from Equation (2), when the OR circuit is used, if discrimination thresholds are adjusted to reduce  $P(1/0)$ ,  $P(0/1)$  is a main factor of an error. However, as indicated by Equation (2), since  $P(0/1)$  has effect on  $BER_{OR}$  only in the order of a square,  $BER_{OR} \sim P(0/1)^2 \sim BER_{RAW}^2$ .  $BER_{RAW}$  is an error rate at the time 20 when usual optical reception is performed using a single discriminator.

An output of the operational circuit 14 constituted by an AND circuit is examined. Fig. 2C is a diagram of a truth table of the AND circuit.

25 Under conditions identical with those for the operational circuit 14 constituted by the OR circuit, an error occurs in an output result of the operational circuit 14 constituted by the AND circuit in the following cases.

(a) In the case where logical "0" is input to an optical signal

When both discrimination results of the discriminators 13a and 13b are wrong

5 (b) In the case where logical "1" is input to an optical signal

When one of discrimination results of the discriminators 13a and 13b is wrong

10 When both discrimination results of the discriminators 13a and 13b are wrong

In these cases, if an approach same as that for deriving Equation (1) when the operational circuit 14 is the OR circuit is performed, an error rate  $BER_{AND}$  corresponding to Equation (2) at the time when the 15 operational circuit 14 is the AND circuit can be represented by

$$BER_{AND} = \frac{1}{2}[2P(0/1) - P(0/1)^2 + P(1/0)^2] \quad (3)$$

A condition of a mark rate and conditions of a threshold level and the like are identical with those in 20 the case of the OR circuit.

Fig. 3 is a graph of a bit error rate characteristic with respect to a change in a threshold. More specifically, Fig. 3 is a graph of a result obtained by calculating a bit error rate of a discrimination output when a signal 25 amplitude of electrical signals input to the discriminators 13a and 13b is set to "1" and standard deviations (equivalent to noise components)  $\sigma_0$  and  $\sigma_1$  of logical "0" and logical "1" are set as  $\sigma_0=\sigma_1=0.1$ . In the figure, the threshold is on the abscissa and the bit error rate is on 30 the ordinate.

In the figure, when usual optical reception is performed using a single discriminator, the bit error rate

takes a minimum value when the threshold is a standard value (0.5). On the other hand, when the OR circuit is used as the operational circuit 14, the bit error rate takes a minimum value when the threshold is a value  
 5 slightly higher than the standard value (in the figure, around 0.6). Moreover, the minimum value is reduced in the order of  $10^{-2}$  compared with the time when the single discriminator is used. Thus, it is evident that sensitivity of discrimination is improved.

10 This sensitivity improvement effect is attained by setting the threshold of discrimination slightly higher than the standard value, or reducing  $P(1/0)$ , which is the probability that logical "0" is misjudged as logical "1".

15 Similarly, when the AND circuit is used as the operational circuit 14, the bit error rate takes a minimum value when the threshold is slightly higher than the standard value (in the figure, around 0.4). The minimum value is reduced in the order of  $10^{-2}$  compared with the time when the single discriminator is used. Sensitivity of  
 20 discrimination is improved.

The sensitivity improvement effect in the case of the AND circuit is opposite to that in the case of the OR circuit. The sensitivity improvement effect is attained by setting the threshold of discrimination slightly lower than the standard value, or reducing  $P(0/1)$ , which is the probability that logical "1" is misjudged as logical "0".  
 25

30 The characteristic shown in Fig. 3 indicates an example of a calculation result. For example, even when a noise distribution is different from the calculation result in Fig. 3 or even when a noise distribution is an asymmetric distribution ( $\sigma_0 \neq \sigma_1$ ), by selecting predetermined values as  $V_{th1}$  and  $V_{th2}$ , it is possible to improve reception sensitivity compared with that at the time when usual

optical reception is performed using the single discriminator.

As explained above, according to the present embodiment, an optical input signal is divided into a plurality of paths, the optical input signals after division are converted into electrical signals, a predetermined operation is performed by the OR circuit, the AND circuit, or the like based on discrimination results obtained by discriminating, based on predetermined thresholds, the electrical signals output from the OE converters. Thus, it is possible to improve reception sensitivity of the optical receiver.

According to the present embodiment, an optical input signal is divided into two paths by the optical divider. However, the number of paths is not limited to two. The optical input signal may be divided into three or more arbitrary paths.

#### Second Embodiment

Fig. 4 is a block diagram of a structure of an optical receiver according to a second embodiment of the present invention. The optical receiver shown in the figure includes optical amplifiers 40a and 40b for compensating for a fall in an optical level between the optical divider 11 and the OE converters 12a and 12b, respectively, in addition to the components of the optical receiver shown in Fig. 1. The other components are identical with or equivalent to those in the first embodiment. Thus, the components are denoted by the identical reference numerals and signs.

Operations of the optical receiver shown in Fig. 4 are explained below. In the figure, an optical signal transmitted from a not-shown optical transmitter or the like is input to the optical receiver. The optical input

signal received is amplified by the optical amplifier 10 and, then, divided into the two paths, that is, the path 1 and the path 2, by the optical divider 11. The optical signals divided into the two paths are amplified by the 5 optical amplifiers 40a and 40b, respectively. In this case, since optical power is sufficiently amplified by the optical amplifier 10, if an optical loss due to the optical divider 11 is small, deterioration in an optical signal to noise ratio (SNR) due to the optical amplifiers 40a and 40b 10 hardly occurs. Since operations after that are identical with the operations in the first embodiment, explanation of the operations are omitted.

Since noises imparted by the optical amplifiers 40a and 40b are uncorrelated with each other, the optical 15 signals divided into the two paths form an uncorrelated noise distribution.

Therefore, even when the optical amplifiers 40a and 40b for compensating for a fall in a level of the optical signals divided into the two paths are present between the 20 optical divider 11 and the OE converters 12a and 12b, it is possible to improve reception sensitivity of the optical receiver as in the case of the first embodiment using the OE converters 12a and 12b, the discriminators 13a and 13b, and the operational circuit 14.

As explained above, according to the present embodiment, even if the optical receiver includes optical amplifiers for compensating for a fall in a level of optical signals divided into two paths, it is possible to improve reception sensitivity of the optical receiver 30 without deteriorating an optical SNR.

As the optical amplifiers 10, 40a, and 40b, any optical amplifiers such as a rare-earth-doped fiber amplifier and a semiconductor optical amplifier (SOA) may

be used.

According to the present embodiment, an optical input signal is divided into two paths by the optical divider. However, as in the first embodiment, the number of paths is 5 not limited to two and the optical input signal may be divided into three or more arbitrary paths.

### Third Embodiment

Fig. 5 is a block diagram of a structure of an optical receiver according to a third embodiment of the present invention. The optical receiver shown in the figure 10 includes an operational circuit 50 having a function different from that of the operational circuit 14 in the structure of the optical receiver shown in Fig. 1. The optical receiver also includes a bit-error-rate (BER) 15 monitoring unit 51 that monitors an output of the operational circuit 50 and a discrimination-threshold control circuit 52 that controls discrimination thresholds of the discriminators 13a and 13b based on an output of the BER monitoring unit 51. The other components are identical 20 with or equivalent to those in the first embodiment. Thus, the components are denoted by the identical reference numerals and signs.

Operations of the optical receiver shown in Fig. 5 are explained below. It is assumed that conditions such as 25 characteristics and fluctuation in discrimination sensitivity of the discriminators 13a and 13b, uncorrelation of Gaussian noises included in optical signals on respective paths, and mutual independency on discrimination results between the discriminators 13a and 30 13b are identical with those in the first embodiment.

In Fig. 5, an optical signal transmitted from a not-  
shown optical transmitter or the like is input to the  
optical receiver. The optical input signal received is

amplified by the optical amplifier 10 and, then, divided into the two paths, that is, the path 1 and the path 2, by the optical divider 11. The optical input signals divided into the two paths are converted into electrical signals by  
5 the OE converters 12a and 12b and output to the discriminators 13a and 13b. The discriminators 13a and 13b output discrimination results (digital data signals) obtained by discriminating the analog electrical signals based on the predetermined thresholds  $V_{th1}$  and  $V_{th2}$  to the  
10 operational circuit 50. The operational circuit 50 outputs an operation result obtained by performing a predetermined operation based on the discrimination results of the discriminators 13a and 13b. On the other hand, the BER monitoring unit 51 monitors a bit error rate based on the  
15 output of the operational circuit 50 and outputs a monitoring result to the BER monitoring unit 51. The discrimination-threshold control circuit 52 controls discrimination thresholds of the discriminators 13a and 13b to reduce the bit error rate.

20 In the first and the second embodiments, thresholds imparted to the discriminators 13a and 13b are determined according to whether the operational circuit 14 is the OR circuit or the AND circuit as described above. On the other hand, according to the present embodiment, thresholds  
25 imparted to the discriminators 13a and 13b are determined based on a bit error rate. In other words, thresholds imparted to the discriminators 13a and 13b are determined first and the operational circuit 50 applies an optimum operational processing based on the thresholds determined.

30 As a simple example, when the operational circuit 50 has a function of an OR circuit and a function of an And circuit, if the thresholds imparted to the discriminators 13a and 13b are set smaller than a standard value, a

function of the operational circuit 50 only has to be switched to the function of the AND circuit. On the other hand, if the thresholds imparted to the discriminators 13a and 13b are set larger than the standard value, a function 5 of the operational circuit 50 only has to be switched to the function of the OR circuit.

Besides the method described above, for example, a method of sweeping discrimination thresholds by several points to determine a point at which an error rate is 10 minimized may be adopted. Alternatively, discrimination thresholds may be swept in a try and error manner. Thresholds imparted to the discriminators 13a and 13b do not always have to be set equal.

These methods are particularly effective when a noise 15 distribution of a signal is not Gaussian noise, when the noise distribution is asymmetric noise, or when a discrimination sensitivity difference between discriminators is large.

As explained above, according to the present 20 embodiment, a function of the operational circuit is switched to, for example, the OR circuit or the AND circuit. Thus, even when an input condition of an optical signal changes, it is possible to maintain predetermined reception sensitivity.

According to the present embodiment, an optical input 25 signal is divided into two paths by the optical divider. However, as in the other embodiments, the number of paths is not limited to two. The optical input signal may be divided into three or more arbitrary paths.

#### 30 Fourth Embodiment

Fig. 6 is a block diagram of a structure of an optical receiver according to a fourth embodiment of the present invention. The optical receiver shown in the figure

includes an optical polarization divider 61 instead of the optical divider 11 in the structure of the optical receiver shown in Fig. 1. The optical receiver also includes, between the optical polarization divider 61 and the optical 5 amplifier 10, an optical polarization controller 60 that controls a plane of polarization of an optical signal. Moreover, the optical receiver includes power monitoring units 63a and 63b that monitor optical power of an optical signal via optical couplers 62a and 62b provided in 10 respective paths between the optical polarization divider 61 and the OE converters 12a and 12b. The other components are identical with or equivalent to those in the first embodiment. Thus, the components are denoted by the identical reference numerals and signs.

Operations of an optical receiver shown in Fig. 6 are explained below. In the figure, an optical signal transmitted from a not-shown optical transmitter is input to the optical receiver. The optical input signal received is amplified by the optical amplifier 10 and, then, 15 subjected to polarization control by the optical polarization controller 69, and divided into the two paths, that is, the path 1 and the path 2, by the optical polarization divider 61.

The optical signals divided into the path 1 and the 25 path 2 are tapped by the optical couplers 62a and 62b. Optical power  $P_1$  of the path 1 and optical power  $P_2$  of the path 2 are monitored by the power monitoring units 63a and 63b. A control circuit 64 controls the optical polarization controller 60 based on information from the 30 power monitoring such that  $P_1$  is always equal to  $P_2$ .

With the structure described above, it is possible to stably receive the optical signals on the two paths.

ASE (Amplified Spontaneous Emission) noise components

included in the two paths subjected to polarization division are in a polarization state in which the ASE noise components are orthogonal to each other. The ASE noise components are amplified independently from each other when 5 the ASE noise components are optically amplified. Thus, it is experimentally indicated that the ASE noise components forms an uncorrelated noise distribution.

Therefore, even when the optical polarization controller 69, the optical polarization divider 61, and the 10 optical couplers 62a and 62b are present on paths of an optical signal, as in the first embodiment, there is an effect that reception sensitivity is improved by the OE converters 12a and 12b, the discriminators 13a and 13b, and the operational circuit 14.

15 As explained above, according to the present embodiment, the optical polarization controller is controlled such that respective output values of optical signal powers on a plurality of paths monitored by optical monitors are substantially equal. This makes it possible 20 to stably receive optical signals on the paths.

According to the present embodiment, an optical input signal is divided into two paths by the optical divider. However, as in the other embodiments, the number of paths is not limited to two. The optical input signal may be 25 divided into three or more arbitrary paths.

#### Fifth Embodiment

Fig. 7 is a block diagram of a structure of an optical receiver according to a fifth embodiment of the present invention. The optical receiver shown in the figure 30 includes soft decision discriminators 70a and 70b instead of the discriminators 13a and 13b in the structure of the optical receiver shown in Fig. 5. The optical receiver also includes a plurality of operational circuits 71-1, 71-

2, ..., 71-n in association with a plurality of threshold levels and a BER monitoring unit 72 that can monitor bit error rates of a plurality of operational circuit outputs. Moreover, the optical receiver includes a path selecting unit 73 that selects an optimum discrimination result out of output results of the operational circuits 71-1, 71-2, ..., 71-n. The other components are identical with or equivalent to those in the third embodiment. The components are denoted by the identical reference numerals 10 and signs.

Operations of the optical receiver shown in Fig. 7 are explained below. It is assumed that conditions such as characteristics and fluctuation in discrimination sensitivity of the discriminators 13a and 13b, 15 uncorrelation of Gaussian noises included in optical signals of respective paths, and mutual independency on discrimination results between the discriminators 13a and 13b are identical with those in the third embodiment.

In Fig. 7, an optical signal transmitted from a not-20 shown optical transmitter or the like is input to the optical receiver. The optical input signal received is amplified by the optical amplifier 10 and, then, divided into the two paths, that is, the path 1 and the path 2, by the optical divider 11. The optical input signals divided 25 into the two paths are converted into electrical signals by the OE converters 12a and 12b and output to the soft decision discriminators 70a and 70b. The soft decision discriminators 70a and 70b output discrimination results (digital data signals) obtained by discriminating the 30 analog electrical signals based on predetermined thresholds  $V_{th1}, V_{th1}, \dots, V_{th2}$  to the operational circuits 71-1, 71-2, ..., 71-n. Each of the operational circuits 71-1, 71-2, ..., 71-n outputs an operation result obtained by

performing a predetermined operation based on the discrimination results of the soft decision discriminators 70a and 70b.

On the other hand, the BER monitoring unit 72 monitors  
5 a bit error rate based on the respective outputs of the operational circuits 71-1, 71-2, ..., 71-n and outputs monitoring results to the path selecting unit 73. The path selecting unit 73 selects and outputs an operational circuit output having a smaller bit error rate.

10 According to the present embodiment, unlike the fourth embodiment, the soft decision discriminators are used as the discriminators and an optimum discrimination result is selected out of outputs of the operational circuits. This makes it possible to further reduce a bit error rate while  
15 improving reception sensitivity.

As explained above, according to the present embodiment, the soft decision discriminating means are used to discriminate electrical signals and an output result having a low error rate is selected based on monitoring  
20 information of a bit error rate. This makes it possible to further reduce the bit error rate while improving reception sensitivity.

According to the present embodiment, an optical input signal is divided into two paths by the optical divider.  
25 However, as in the other embodiments, the number of paths is not limited to two. The optical input signal may be divided into three or more arbitrary paths.

#### INDUSTRIAL APPLICABILITY

30 As described above, the optical receiver according to the present invention is useful as an optical receiver applicable to very-high-speed/very-long-distance optical transmission systems and particularly suitable, for example,

when it is desired to improve reception sensitivity of these systems.